

CLAIMS

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1. Computer system, in which a processor, memory, peripheral circuits, and interconnecting wires are formed together over one semiconductor substrate, having: a scan-path interface circuit, which allows for a readout of the storage contents of a prescribed memory or a register within the system through the aforementioned processor by the scan-path method; a switching circuit, which can be accessed by the aforementioned processor, and selectively switches the aforementioned scan-path interface circuit between the enabled state and the disabled state according to the prescribed switch control information given by the aforementioned processor; and a program storage means, which stores the program for the aforementioned processor to enable processing to give the aforementioned switch control information to the aforementioned switching circuit.

2. Computer system described in Claim 1, characterized by the aforementioned switching circuit having: a register for holding the aforementioned switch control information which can be accessed by the aforementioned processor; and a gate circuit, which is provided in a signal path that is connected to the aforementioned scan-path interface circuit and has an open state or a closed state according to the contents of the aforementioned register.

3. Computer system described in Claim 1 characterized by the aforementioned switching circuit having: a number of password registers for holding the aforementioned switch control

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